

Наличие и актуальные цены на

VFD-250P-48

https://www.mean-well.ru/store/VFD-250P-48/







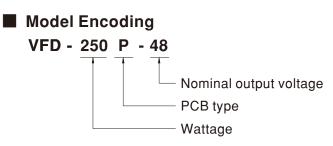


Features

- 4" x 2" miniature size
- 48Vdc input (20~55Vdc)
- 3-phase switches with sensors in one unit for external control (control board VFD-CB sold sperately)
- · High peak current up to 200% and 5 seconds
- · Fanless design for silent operation and long lifetime
- · Protections : Short circuit / OCP
- Internal sensors feed out for control : Current sensor - motor torque control DC bus voltage sensor - OVP/UVP Temperature sensor - OTP
- · -30~+70 $^\circ \rm C$ wider operating temperature
- Suitable for 3-phase motor drive (e.g. BLDC, Induction motor, SynRM)
- 3 years warranty

Description

The VFD-250P-48 is an universal variable frequency drive power module providing integrated 3-phase switches with gate drivers and basic VFD sensors such as three phase output current and temperature sensors. This product can be implemented for a three phase motor drive solution by coordinating with an external motor drive controller in logic level and analog I/O. The three phase motor output is supported up to 55Vdc with 200% peak current capability. The compact size of 4"x2"and fanless design makes it easy to be integrated into all kinds of motor system. The VFD-250P-48 is suitable for three-phase motor drive, such as BLDC, Induction motor, and SynRM applications.





Applications

- HVAC
- Fan
- Water/Air pump
- Power tools
- Conveyor
- Automatic door
- · Fitness equipment

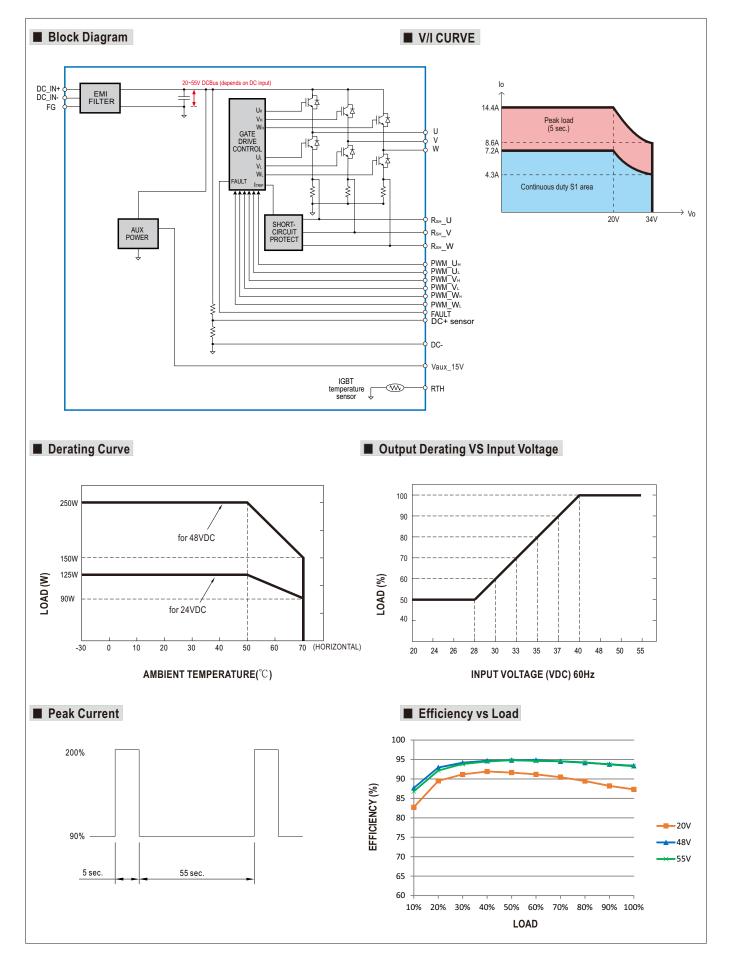




SPECIFICATION

MODEL NO.			VFD-250P-48			
	VOLTAGE RANGE(UVW)		55V Max, line-to-line voltage 0~34V adjusted with modulated PWM, suitable for 48V class motor.			
OUTPUT (Note1,2,3,4)		Rated	7.2A			
	CURRENT	Peak	14.4A for 5 seconds			
	RATED POWER	R	350W			
	EFFICIENCY		93%			
	PMW FREQUENCY		2.5KHz ~ 15KHz			
INPUT	RATED INPUT VOLTAGE		20 ~ 55VDC			
	RATED INPUT CURRENT		5.6A/48VDC Typ.			
	3-PHASE PWM CONTROL		PWM control signal to gate driver for IGBTs. (CN93, PIN8~13) 3.3V TTL/CMOS input : High (>2.7V) : IGBT ON ; Low (<0.4V) : IGBT OFF			
	3- PHASE PWM	CONTROL	Built-in $6m\Omega$ low-side shunt resisor (each phase), (CN93, PIN4~6)			
FUNCTION	DC BUS VOLTA	AGE SENSOR	DC BUS voltage sensor c	output(DC+ sensor, CN93 of PIN1): 2	2.5V@DC BUS 48V	
(Note.5)	THERMAL SEN	ISOR	Built-in 10K Ω NTC for sensing IGBTs operating temperature. (TSM2A103F34D1R (Thinking Electronic), PIN3 of CNS			
	FAULT SIGNAL		Inverter fault signal(Short circuit/OCP, CN93,PIN7). 3.3V TTL/CMOS output : Normal High (>3V) ; Abnormal : Low (<0.5V)			
	AUXILIARY POWER		Non-isolated 15V output power for external control board (CN93, PIN14 to PIN2) 15V @ 0.2A ; Tolerance \pm 0.5V, Ripple 1Vp-p max			
PROTECTION	SHORT CIRCU	IT	Protection type : Shut down o/p voltage, re-power on to recover			
	WORKING TEMP.		-30 ~ +70°C (Refer to "Dreating Curve")			
ENVIRONMENT	WORKING HUI	MIDITY	20 ~ 90% RH non-condensing			
	STORAGE TEMP., HUMIDITY		-40 ~ +85 $^\circ\mathrm{C}$, 10 ~ 95% RH non-condensing			
	VIBRATION		10 ~ 500Hz, 2G 10min./1cycle, period for 60min. each along X, Y, Z axes			
	SAFETY STANDARDS		CB IEC61800-5-1,TUV/BS EN/EN61800-5-1,EAC TP TC004 approved			
			Parameter	Standard	Test Level / Note	
	EMCEMISSION		Conducted	BS EN/EN IEC61800-3	Class A, C2	
			Radiated	BS EN/EN IEC61800-3	Class A, C2	
			BS EN/EN IEC61800-3, second environment			
SAFETY & EMC			Parameter	Standard	Test Level /Note	
			ESD	BS EN/EN61000-4-2	Level 3, 8KV air ; Level 2, 4KV contact	
	EMC IMMUNITY		Radiated	BS EN/EN IEC61000-4-3	Level 3	
			EFT/Burest	BS EN/EN61000-4-4	Level 3	
			Surge	BS EN/EN61000-4-5	Level 2, 1KV/Line-Earth ; Level 2, 0.5KV/Line-Line	
			Conducted	BS EN/EN61000-4-6	Level 3	
			Magnetic Field	BS EN/EN61000-4-8	Level 2	
	MTBF		3826.9K hrs min.Telcordia SR-332 (Bellcore) ; 301.4K hrs min.MIL-HDBK-217F (25 $^\circ C)$			
OTHERS	DIMENSION (L*W*H)		101.6*50.8*28.6mm			
UTHERS	PACKING		0.09kg;96pcs/9.64kg/1.44CUFT			
NOTE	 2. Refer to p 3. Efficience 4. All param 5. Please re 	beak current ca y is tested with neters NOT spe efer to"Function	pability in "V/I curve". inductive load at rated cially mentioned are me al Manual" for more de	current and full power. easured at 48VDC input, ratec tails.	used for 12V/24V class motor. Hoad and 25°C of ambient temperature. /www.meanwell.com/serviceDisclaimer.asp	



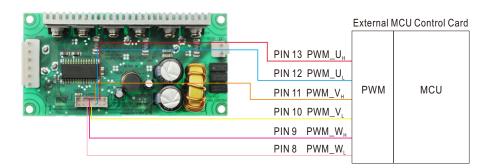




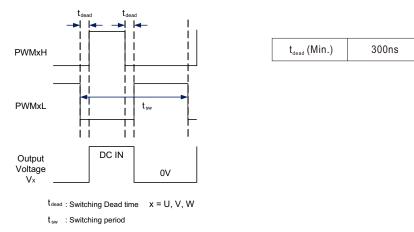
Function Manual

1. 3-phase PWM Control (CN93, PIN8~13)

VFD-250P-48 provides six-switch circuit by using 3 half-bridge IGBTs. IGBTs of each phase is controlled by PWM_U_H/U_L , PWM_V_H/V_L and PWM_W_H/W_L (PIN 8~13). The input requirement for PWM is compatible with both TTL and CMOS 3.3V signals. Please refer to the diagram below.

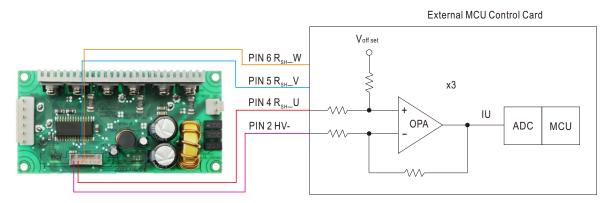


WARNING: It is necessary to keep minimum dead-time 300ns between the upper and lower switch of each phase.

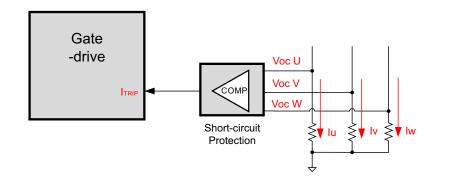


2. 3-phase Current Detection & Overcurrent Protection (CN93, PIN4~6)

Low-side shunt resistors $6m\Omega$ are installed on each phase of VFD-250P-48 for current measurement and short-circuit detection. It's suggested to shorten the length of external detection circuit and detect the signal with a OPAs. Please refer to diagram below.



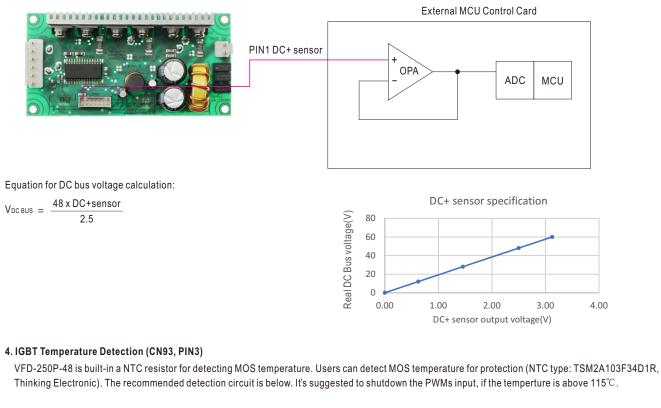
If output current exceeds 200% of rated value, the internal protection circuit will be triggered and shut down the gate driver for protection.

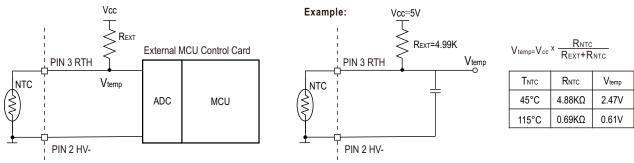




3. DC BUS Voltage Detection (CN93, PIN1)

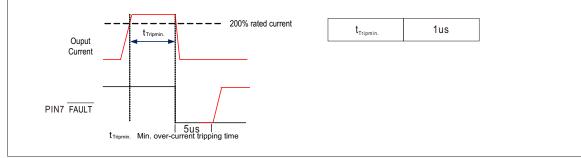
VFD-250P-48 is build-in with DC bus voltage sensor(DC+ sensor, PIN 1). The sensor provides a 2.5V output when DC bus voltage is at 48V. It's suggested to detect the signal by OPAs. When the voltage of the DC bus exceed 60V, the PWM input signal must shut down for protection.





5. Fault signal

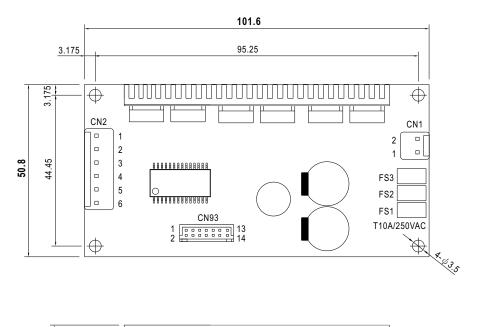
The FAULT signal would be active(active-low) to notify external controller or circuit, if VFD-250P-48 encounter the overcurrent state and keep the state for minimum overcurrent tripping time

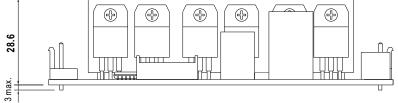




Mechanical Specification

(Unit: mm , tolerance ± 1mm)





AC Input Connector (CN1) : JST B2P-VH or equivalent

Pin No.	Assignment
1	DC INPUT +
2	DC INPUT -

Mating housing: JST VHR or equivalent Terminal: JST SVH-21T-P1.1 or equivalent

PWM Output Connector(CN2): JST B6P-VH or equivalent

	-p
Pin No.	Assignment
1,2	U
3,4	V
5,6	W

Mating housing: JST VHR or equivalent Terminal: JST SVH-21T-P1.1 or equivalent

Control Pin NO. Assignment (CN93) : HRS DF11-14DP-2DS or equivalent

Pin No.	Assignment	Pin No.	Assignment
1	DC+ sensor	8	PWM_W _H

1	DC+ Selisor	0	
2	DC-	9	PWM_W
3	RTH	10	PWM_V _H
4	R _{sh} _U	11	PWM_V
5	R _{sh} _V	12	PWM_U _H
6	R _{sh} _W	13	PWM_U
7	FAULT	14	Vaux_15V

Mating housing: HRS DF11-14DS or equivalent Terminal HRS DF11-**SC or equivalent

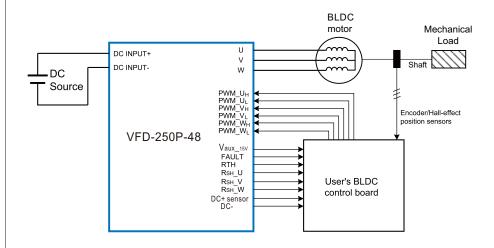


%Control Pin No. Assignment(CN93) :

Pin No.	Function	Description
1	DC+ sensor	DC BUS voltage sensor output 2.5V, reference to pin 2(DC-)
2	DC-	DC BUS voltage sensor output ground
3	RTH	Temperature sensor
4	R _{sh} U	U phase current sensor output
5	R _{sh} _V	V phase current sensor output
6	R _{sh} _W	W phase current sensor output
7	FAULT	Over current detection. Normal > 3V, Abnormal < 0.5V
8	PWM_W _H	W phase high side logic input, on > 2.7V ; off < 0.4V
9	PWM_W	W phase low side logic input, on > 2.7V ; off < 0.4V
10	PWM_V_H	V phase high side logic input, on > 2.7V ; off < 0.4V
11	PWM_V	V phase low side logic input, on > 2.7V ; off < 0.4V
12	PWM_U_H	U phase high side logic input, on > 2.7V ; off < 0.4V
13	PWM_U	U phase low side logic input, on > 2.7V ; off < 0.4V
14	Vaux_15v	Auxiliary voltage output 15V reference to pin2 (DC-). The maximum load current is 0.2A

Application

Application example: BLDC drive application



1. The figure shows the BLDC drive system which set up with VFD-250P-48.

 2.Developers can control the PWM signal of 6-switch by using SPWM or SVPWM, etc. for 3-phase voltage modulation, and build the control method base on the current shunt sensors on 3-phase low-side switch(R_{sH}_U/V/W) and the DC BUS voltage sensor(DC+ sensor) which provided by VFD-250P-48.
 3.Developers select the appropriate BLDC position sensors such as encoder or Hall-effect sensors to fit their applications.

4.It's suggested to install the brake circuit/device at the DC input for avoiding the DC BUS OVP when BLDC is decelerating.

5.It's suggested to shut down the PWM input or connect to brake resistor device for safety when DC Bus voltage is higher than 60V.

6.If VFD-250P-48 were applied non-appropriate control, such as accelerating too quickly or bad current control, it might trig the VFD-250P-48's fault-state to shut down the output voltage(low-level on FAULT pin).



